

T&A-135

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Satoshi SAKAI et al.

Serial No.: not yet assigned

Filed: December 29, 2004

For: METHOD OF PRODUCING SEMICONDUCTOR INTEGRATED
CIRCUIT DEVICE

Group: not yet assigned

Examiner: not yet assigned

PRELIMINARY AMENDMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

December 29, 2004

Sir:

Prior to examination, please amend the above-identified application as
follows.